	Application No.	Applicant(s)
Notice of Allowability	10/790,919 Examiner	YU, TA-LEE Art Unit
	Theresa T. Doan	2814
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to 10/21/04.		
2. The allowed claim(s) is/are <u>25-29,31 and 47-53</u> .		
3. The drawings filed on <u>02 March 2004</u> are accepted by the Examiner.		
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)		
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 10/21/04 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Da 8), 7. ☐ Examiner's Amendo	

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Reasons for Allowance

1. Claims 25-29, 31 and 47-53 are allowed.

2. The following is an examiner's statement of reasons for allowance:

The prior art of record fails to disclose the combination of an integrated multiple vertical NPN transistor ESD protection structure on a semiconductor substrate recited in the base claims 25 and 47. Specifically, the combination of the structure comprising the third regions form multiple emitter elements, the multiple emitter elements being electrically connected by a conductor element with N horizontal stripe conductor elements connected in a contiguous comb like manner by a vertical contact conductor element at one end of the horizontal stripe conductor elements.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Theresa T Doan whose telephone number is (571) 272-1704. The examiner can normally be reached on Monday to Thursday from 8:00AM - 6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, WAEL FAHMY can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TD January 7, 2005.

PHAT X. CAO
PRIMARY EXAMINER